



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Mark G. Johnson et al.

Title: INTEGRATED CIRCUIT INCORPORATING THREE-DIMENSIONAL  
MEMORY ARRAY WITH DUAL OPPOSING DECODER  
ARRANGEMENT

Application No.: 10/774,818

Filed: February 9, 2004

Examiner: Unknown

Group Art Unit: 2812

Atty. Docket No.: 023-0025

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**37 C.F.R. § 1.97(b)**

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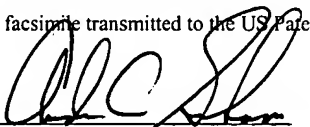
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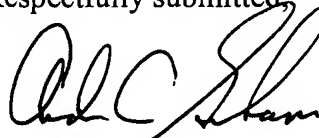
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Respectfully submitted,



Andrew C. Graham, Reg. No. 36,531  
 Attorney for Applicant(s)  
 (512) 338-6313 (direct)  
 (512) 338-6300 (main)  
 (512) 338-6301 (fax)



